

U.S.S.N. 09/961,049

**APPENDIX: Listing of Claims Currently Pending in the Application**

1. (Previously Presented) An intelligent accessory system for identifying a particular electronic device, the system comprising:

- a. a universal base unit having a control circuit, the control circuit having at least one input and at least one output, the universal base unit further comprising a resistor electrically coupled to the at least one input; and
- b. an interface device having both a first connector capable of mechanically and electrically coupling to the universal base unit and a second connector capable of mechanically and electrically coupling to at least one first electronic device, the interface device further comprising a capacitor with a value predetermined to correspond to the at least one first electronic device;

further comprising a comparator electrically coupled in series between the resistor and the control circuit;

wherein when the interface device is coupled to the universal base unit the capacitor and the resistor are electrically coupled in series;

further wherein to identify the at least one first electronic device, the control circuit applies a voltage to the capacitor and measures an exponential voltage decay across the resistor.

2. (Canceled)

3. (Previously Presented) The system of claim 1, further comprising a protection circuit disposed within the interface device.

4. (Original) The system of claim 3, wherein when the at least one first electronic device is identified, the control circuit configures the universal base unit to accommodate the at least one first electronic device.

U.S.S.N. 09/961,049

5. (Original) The system of claim 4, wherein the control unit further comprises a means of correlating a predetermined capacitor value to one of a plurality of electronic devices.

6. (Original) The system of claim 5, wherein the universal base unit further comprises a power conversion circuit.

7. (Original) The system of claim 6, wherein the power conversion circuit is selected from the group consisting of fly back regulator, a step-down, or "buck", regulator, a step-up regulator, a buck-boost regulator, and a resonant regulator.

8. (Original) The device of claim 1, wherein the capacitor is disposed on an integrated circuit semiconductor substrate.

9. (Canceled)

10. (Canceled)

11. (Previously Presented) A universal accessory system, the system comprising:

- a. a control circuit;
- b. a resistance means for providing a resistance value, the resistance means comprising two terminals;
- c. an identification capacitor disposed within an interface means capable of being electrically coupled to the resistance means, the capacitor corresponding to at least one electronic device, the capacitor, when coupled to the resistor, having a characteristic time constant related to the first electronic device; and;
- d. the control circuit further comprising:
  - i. a switch capable of applying a step function voltage across the identification capacitor;
  - ii. means for periodically measuring the voltage across the resistance means;

U.S.S.N. 09/961,049

- iii. means for computing the time elapsed during an exponential decay of the voltage across the resistance means; and
- iv. means for converting the measured stabilizing time to a predetermined identifiable electronic device usage.

12. (Previously Presented) A method for identifying an electronic device, the method comprising the steps of:

- a. providing a universal base unit having a microprocessor, a resistance means coupled electrically to the microprocessor, the universal base unit further comprising a universal connector;
- b. providing an interface means having a capacitor corresponding to a first electronic device, the interface means further comprising a universal mating connector and a connector of accommodating the first electronic device;
- c. coupling the interface means to the universal base unit;
- d. closing a switch to apply a step-function voltage across the capacitor;
- e. sensing an exponential voltage decay across the resistor;
- f. calculating a time corresponding to the exponential voltage decay across the resistor;
- g. associating a set of accommodating parameters associated with the first electronic device based upon the time corresponding to the exponential voltage decay across the resistor.